

SEARCH REQUEST FORM

Scientific and Technical Information Center

Requester's Full Name: Daniel Pava Examiner #: 68999 Date: May 31/01
Art Unit: 2183 Phone Number 305 9696 Serial Number: 09/234,427
Mail Box and Bldg/Room Location: PKII 2A05 Results Format Preferred (circle): PAPER DISK E-MAIL
or

If more than one search is submitted, please prioritize searches in order of need.

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: Integrated Digital Signal processor/general purpose CPU with
Inventors (please provide full names): Amos Intrater et al *Shared Invention*

Earliest Priority Filing Date: 01/18/90

For Sequence Searches Only Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.

This is a reissue case, and I need
a "litigation search".

please notify me when complete the search.

please go ahead the search now.

surrendered patent # 5,630,153.

05-31-01 P

05-31-

STAFF USE ONLY

	Type of Search	Vendors and cost where applicable
Searcher: <u>J Green</u>	NA Sequence (#) _____	STN _____
Searcher Phone #: <u>6-4767</u>	AA Sequence (#) _____	Dialog <u>11.53</u> <u>2 min</u>
Searcher Location: <u>4B40</u>	Structure (#) _____	Questel/Orbit <u>913</u> <u>2 min</u>
Date Searcher Picked Up: <u>5/31/01</u>	Bibliographic _____	Dr.Link _____
Date Completed: <u>5/31/01</u>	Litigation <input checked="" type="checkbox"/>	Lexis/Nexis <u>7 min</u>
Searcher Prep & Review Time: <u>5</u>	Fulltext _____	Sequence Systems _____
Clerical Prep Time: _____	Patent Family _____	WWW/Internet _____
Online Time: <u>11</u>	Other _____	Other (specify) _____

us5630153/pn

LGST	1
CRXX	1
LITA	0
PAST	0

** SS 1 : Results 2

Search statement 2

?prt ssl max 1-2

1/2 LGST (1/1) - (C) LEGSTAT
PN - US 5630153 [US5630153]
AP - US 317783/94 19941004 [1994US-0317783]
DT - US-P
ACT - 19941004 US/AE-A
APPLICATION DATA (PATENT)
{US 317783/94 19941004 [1994US-0317783]}
- 19970513 US/A
PATENT
- 19990323 US/RF
REISSUE APPLICATION FILED
990120
UP - 1999-15

2/2 CRXX (1/1) - (C) CLAIMS/RRX
AN - 2841754
PN - 5,630,153 A 19970513 [US5630153]
PA - National Semiconductor Corp
PT - E (Electrical)
ACT - 19990120 REISSUE REQUESTED
Issue Date of O.G.: 19990323
Reissue Request Number: 09/234427
Examination Group responsible for Reissue process: 1641
UP - 1999-00

S1 1 PN US 5630153"
?t s1/9/1

1/9/1

DIALOG(R) File 345:Inpadoc/Fam.& Legal Stat
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10066824

Basic Patent (No,Kind,Date): EP 442041 A2 19910821 <No. of Patents: 007>

PATENT FAMILY:

EUROPEAN PATENT OFFICE (EP)

Patent (No,Kind,Date): EP 442041 A2 19910821

INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English; French; German)

Patent Assignee: NAT SEMICONDUCTOR CORP (US)

Author (Inventor): INTRATER AMOS (HK); DORON MOSHE (US); INTRATER
GIDEON (IL); EPSTEIN LEV (IL)

Priority (No,Kind,Date): US 467148 A 19900118

Applic (No,Kind,Date): EP 90120850 A 19901031

Designated States: (National) DE; FR; GB; IT

IPC: * G06F-015/78; G06F-009/38

Derwent WPI Acc No: ; G 91-246848

Language of Document: English

Patent (No,Kind,Date): EP 843254 A2 19980520

INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English; French; German)

Patent Assignee: NAT SEMICONDUCTOR CORP (US)

Author (Inventor): INTRATER AMOS (CN); DORON MOSHE (IL); INTRATER
GIDEON (IL); EPSTEIN LEV (IL)

Priority (No,Kind,Date): EP 90120850 A3 19901031; US 467148 A
19900118

Applic (No,Kind,Date): EP 97120341 A 19901031

Designated States: (National) DE; FR; GB; IT

IPC: * G06F-009/38

Derwent WPI Acc No: * G 91-246848; G 98-263467; G 98-263467

Language of Document: English

Patent (No,Kind,Date): EP 442041 A3 19910904

INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English; French; German)

Patent Assignee: NAT SEMICONDUCTOR CORP (US)

Author (Inventor): INTRATER AMOS (HK); DORON MOSHE (US); INTRATER
GIDEON (IL); EPSTEIN LEV (IL)

Priority (No,Kind,Date): US 467148 A 19900118

Applic (No,Kind,Date): EP 90120850 A 19901031

Designated States: (National) DE; FR; GB; IT

IPC: * G06F-015/78; G06F-009/38

Language of Document: English

Patent (No,Kind,Date): EP 843254 A3 19990818

INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English; French; German)

Patent Assignee: NAT SEMICONDUCTOR CORP (US)

Author (Inventor): INTRATER AMOS (CN); DORON MOSHE (IL); INTRATER
GIDEON (IL); EPSTEIN LEV (IL)

Priority (No,Kind,Date): EP 90120850 A3 19901031; US 467148 A
19900118

Applic (No,Kind,Date): EP 97120341 A 19901031

Designated States: (National) DE; FR; GB; IT

IPC: * G06F-009/38

Derwent WPI Acc No: * G 91-246848; G 98-263467

Language of Document: English

EUROPEAN PATENT OFFICE (EP)

Legal Status (No,Type,Date,Code,Text):

EP 442041 P 19900118 EP AA PRIORITY (PATENT

APPLICATION) (PRIORITAET) (PATENTANMELDUNG)

EP 442041	P	US 467148 A 19900118 19901031 EP AE EP-APPLICATION (EUROPAEISCHE ANMELDUNG) EP 90120850 A 19901031
EP 442041	P	19910821 EP AK DESIGNATED CONTRACTING STATES IN AN APPLICATION WITHOUT SEARCH REPORT (IN EINER ANMELDUNG OHNE RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN)
EP 442041	P	DE FR GB IT 19910821 EP A2 PUBLICATION OF APPLICATION WITHOUT SEARCH REPORT (VEROEFFENTLICHUNG DER ANMELDUNG OHNE RECHERCHENBERICHT)
EP 442041	P	19910904 EP AK DESIGNATED CONTRACTING STATES IN A SEARCH REPORT (IN EINEM RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN)
EP 442041	P	DE FR GB IT 19910904 EP A3 SEPARATE PUBLICATION OF THE SEARCH REPORT (ART. 93) (GESONDERTE VEROEFFENTLICHUNG DES RECHERCHENBERICHTS (ART. 93))
EP 442041	P	19920624 EP 17P REQUEST FOR EXAMINATION FILED (PRUEFUNGSANTRAG GESTELLT) 920424
EP 442041	P	19950315 EP 17Q FIRST EXAMINATION REPORT (ERSTER PRUEFUNGSBESCHIED) 950127
EP 442041	P	19980513 EP 18R REFUSED (ZURUECKGEWIESEN)
EP 442041	P	971225 19980520 EP AH DIVISIONAL APPLICATION (ART. 76) IN: (TEILANMELDUNG (ART. 76) IN:) EP 843254 P
EP 843254	P	19900118 EP AA PRIORITY (PATENT APPLICATION) (PRIORITAET (PATENTANMELDUNG))
EP 843254	P	US 467148 A 19900118 19901031 EP AA DIVIDED OUT OF (AUSSCHIEDUNG AUS) EP 90120850 A3 19901031
EP 843254	P	19901031 EP AE EP-APPLICATION (EUROPAEISCHE ANMELDUNG) EP 97120341 A 19901031
EP 843254	P	19980520 EP AC DIVISIONAL APPLICATION (ART. 76) OF: (TEILANMELDUNG (ART. 76) AUS:) EP 442041 P
EP 843254	P	19980520 EP AK DESIGNATED CONTRACTING STATES IN AN APPLICATION WITHOUT SEARCH REPORT: (IN EINER ANMELDUNG OHNE RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN)
EP 843254	P	DE FR GB IT 19980520 EP A2 PUBLICATION OF APPLICATION WITHOUT SEARCH REPORT (VEROEFFENTLICHUNG DER ANMELDUNG OHNE RECHERCHENBERICHT)
EP 843254	P	19980520 EP 17P REQUEST FOR EXAMINATION FILED (PRUEFUNGSANTRAG GESTELLT) 971125
EP 843254	P	19990818 EP AK DESIGNATED CONTRACTING STATES IN A SEARCH REPORT: (IN EINEM RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN)

JAPAN (JP)

Patent (No,Kind,Date): JP 3227177 A2 19911008
 INTEGRATED DIGITAL SIGNAL PROCESSOR/UNIVERSAL CPU HAVING COMMON
 INTERNAL MEMORY (English)
 Patent Assignee: NAT SEMICONDUCTOR CORP
 Author (Inventor): AMOSU INTORATA; MOSHIE DORON; GIDEON INTORATA; REBU
 EPUSHIYUTAIN
 Priority (No,Kind,Date): US 467148 A 19900118
 Applic (No,Kind,Date): JP 90311677 A 19901119
 IPC: * H04N-001/40; G06J-003/00; H04M-011/00
 Language of Document: Japanese

KOREA, REPUBLIC (KR)

Patent (No,Kind,Date): KR 211549 B1 19990802
 INTERGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
 INTERNAL MEMORY (English)
 Patent Assignee: NAT SEMICONDUCTOR CORP (US)
 Author (Inventor): INTRATER AMOS (IL); INTRATER GIDEON (IL); EPSTEIN
 LEV (IL)
 Priority (No,Kind,Date): US 467148 A 19900118
 Applic (No,Kind,Date): KR 9018430 A 19901114
 IPC: * G06F-015/16
 Derwent WPI Acc No: * G 91-246848; G 98-263467
 Language of Document: Korean

UNITED STATES OF AMERICA (US)

Patent (No,Kind,Date): US 5630153 A 19970513
 INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
 INTERNAL MEMORY (English)
 Patent Assignee: NAT SEMICONDUCTOR CORP (US)
 Author (Inventor): INTRATER AMOS (HK); DORON MOSHE (US); INTRATER
 GIDEON (IL); EPSTEIN LEV (IL); VALENTATEN MAURICE (DE); GREISS
 ISRAEL (IL)
 Priority (No,Kind,Date): US 317783 A 19941004; US 11102 B1
 19930129; US 467148 B1 19900118
 Applic (No,Kind,Date): US 317783 A 19941004
 National Class: * 395800000; 395290000; 395306000; 364DIG001;
 364DIG002
 IPC: * G06F-009/26; G06F-009/40; G06F-009/44; G06F-013/36
 Derwent WPI Acc No: * G 91-246848; G 98-263467
 Language of Document: English

UNITED STATES OF AMERICA (US)

Legal Status (No,Type,Date,Code,Text):
 US 5630153 P 19900118 US AA PRIORITY
 US 467148 B1 19900118
 US 5630153 P 19930129 US AA PRIORITY
 US 11102 B1 19930129
 US 5630153 P 19941004 US AE APPLICATION DATA (PATENT)
 (APPL. DATA (PATENT))
 US 317783 A 19941004
 US 5630153 P 19970513 US A PATENT
 US 5630153 P 19990323 US RF REISSUE APPLICATION FILED
 (REISSUE APPL. FILED)
 990120

5,630,153

<=2> GET 1st DRAWING SHEET OF 8

May 13, 1997

Integrated digital signal processor/general purpose CPU with
shared internal memory

REISSUE: Reissue Application filed Jan. 20, 1999 (O.G. Mar. 23, 1999) Ex. Gp.:
1641; Re. S.N. 09/234,427

CORE TERMS: general purpose, memory, digital, processor, array, bus, module,
operand, bit, data processing...

LEXIS-NEXIS
Library: PATENT
File: ALL

5,630,153 OR 5630153

Your search request has found no CASES.

To edit the above request, use the arrow keys. Be sure to move the cursor to the end of the request before you enter it.

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LEXIS-NEXIS
Library: PATENT
File: CASES

5,630,153 OR 5630153

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LEXIS-NEXIS
Library: PATENT
File: JNLS

5,630,153 OR 5630153

Your search request has found no STORIES.

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LEXIS-NEXIS
Library: NEWS
File: CURNWS

1/9/1

DIALOG(R) File 345:Inpadoc/Fam.& Legal Stat
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10066824

Basic Patent (No,Kind,Date): EP 442041 A2 910821 <No. of Patents: 006>

PATENT FAMILY:

EUROPEAN PATENT OFFICE (EP)

Patent (No,Kind,Date): EP 442041 A2 910821
INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English; French; German)
Patent Assignee: NAT SEMICONDUCTOR CORP (US)
Author (Inventor): INTRATER AMOS (HK); DORON MOSHE (US); INTRATER
GIDEON (IL); EPSTEIN LEV (IL)
Priority (No,Kind,Date): US 467148 A 900118
Applic (No,Kind,Date): EP 90120850 A 901031
Designated States: (National) DE; FR; GB; IT
IPC: * G06F-015/78; G06F-009/38
Derwent WPI Acc No: ; G 91-246848
Language of Document: English

Patent (No,Kind,Date): EP 843254 A2 980520
INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English; French; German)
Patent Assignee: NAT SEMICONDUCTOR CORP (US)
Author (Inventor): INTRATER AMOS (CN); DORON MOSHE (IL); INTRATER
GIDEON (IL); EPSTEIN LEV (IL)
Priority (No,Kind,Date): EP 90120850 A3 901031; US 467148 A
900118
Applic (No,Kind,Date): EP 97120341 A 901031
Designated States: (National) DE; FR; GB; IT
IPC: * G06F-009/38
Derwent WPI Acc No: * G 91-246848; G 98-263467; G 98-263467
Language of Document: English

Patent (No,Kind,Date): EP 442041 A3 910904
INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English; French; German)
Patent Assignee: NAT SEMICONDUCTOR CORP (US)
Author (Inventor): INTRATER AMOS (HK); DORON MOSHE (US); INTRATER
GIDEON (IL); EPSTEIN LEV (IL)
Priority (No,Kind,Date): US 467148 A 900118
Applic (No,Kind,Date): EP 90120850 A 901031
Designated States: (National) DE; FR; GB; IT
IPC: * G06F-015/78; G06F-009/38
Language of Document: English

Patent (No,Kind,Date): EP 843254 A3 990818
INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English; French; German)
Patent Assignee: NAT SEMICONDUCTOR CORP (US)
Author (Inventor): INTRATER AMOS (CN); DORON MOSHE (IL); INTRATER
GIDEON (IL); EPSTEIN LEV (IL)
Priority (No,Kind,Date): EP 90120850 A3 901031; US 467148 A
900118
Applic (No,Kind,Date): EP 97120341 A 901031
Designated States: (National) DE; FR; GB; IT
IPC: * G06F-009/38
Derwent WPI Acc No: * G 91-246848; G 98-263467
Language of Document: English

EUROPEAN PATENT OFFICE (EP)

Legal Status (No,Type,Date,Code,Text):

EP 442041	P	900118	EP AA	PRIORITY (PATENT APPLICATION)
				(PRIORITAET (PATENTANMELDUNG))
			US 467148 A	900118
EP 442041	P	901031	EP AE	EP-APPLICATION (EUROPAEISCHE
				ANMELDUNG)
			EP 90120850 A	901031
EP 442041	P	910821	EP AK	DESIGNATED CONTRACTING STATES IN

			AN APPLICATION WITHOUT SEARCH REPORT (IN EINER ANMELDUNG OHNE RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN) DE FR GB IT
EP 442041	P	910821	EP A2 PUBLICATION OF APPLICATION WITHOUT SEARCH REPORT (VEROEFFENTLICHUNG DER ANMELDUNG OHNE RECHERCHENBERICHT)
EP 442041	P	910904	EP AK DESIGNATED CONTRACTING STATES IN A SEARCH REPORT (IN EINEM RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN) DE FR GB IT
EP 442041	P	910904	EP A3 SEPARATE PUBLICATION OF THE SEARCH REPORT (ART. 93) (GESONDERTE VEROEFFENTLICHUNG DES RECHERCHENBERICHTS (ART. 93))
EP 442041	P	920624	EP 17P REQUEST FOR EXAMINATION FILED (PRUEFUNGSANTRAG GESTELLT) 920424
EP 442041	P	950315	EP 17Q FIRST EXAMINATION REPORT (ERSTER PRUEFUNGSBESCHEID) 950127
EP 442041	P	980513	EP 18R REFUSED (ZURUECKGEWIESEN) 971225
EP 442041	P	980520	EP AH DIVISIONAL APPLICATION (ART. 76) IN: (TEILANMELDUNG (ART. 76) IN:) EP 843254 P
EP 843254	P	900118	EP AA PRIORITY (PATENT APPLICATION) (PRIORITAET (PATENTANMELDUNG)) US 467148 A 900118
EP 843254	P	901031	EP AA DIVIDED OUT OF (AUSSCHIEDUNG AUS) EP 90120850 A3 901031
EP 843254	P	901031	EP AE EP-APPLICATION (EUROPAEISCHE ANMELDUNG) EP 97120341 A 901031
EP 843254	P	980520	EP AC DIVISIONAL APPLICATION (ART. 76) OF: (TEILANMELDUNG (ART. 76) AUS:) EP 442041 P
EP 843254	P	980520	EP AK DESIGNATED CONTRACTING STATES IN AN APPLICATION WITHOUT SEARCH REPORT: (IN EINER ANMELDUNG OHNE RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN) DE FR GB IT
EP 843254	P	980520	EP A2 PUBLICATION OF APPLICATION WITHOUT SEARCH REPORT (VEROEFFENTLICHUNG DER ANMELDUNG OHNE RECHERCHENBERICHT)
EP 843254	P	980520	EP 17P REQUEST FOR EXAMINATION FILED (PRUEFUNGSANTRAG GESTELLT) 971125
EP 843254	P	990818	EP AK DESIGNATED CONTRACTING STATES IN A SEARCH REPORT: (IN EINEM RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN) DE FR GB IT
EP 843254	P	990818	EP A3 SEPARATE PUBLICATION OF THE SEARCH REPORT (ART. 93) (GESONDERTE VEROEFFENTLICHUNG DES RECHERCHENBERICHTS (ART. 93))

JAPAN (JP)

Patent (No,Kind,Date): JP 3227177 A2 911008
INTEGRATED DIGITAL SIGNAL PROCESSOR/UNIVERSAL CPU HAVING COMMON
INTERNAL MEMORY (English)
Patent Assignee: NAT SEMICONDUCTOR CORP
Author (Inventor): AMOSU INTORATA; MOSHIE DORON; GIDEON INTORATA; REBU
EPUSHIYUTAIN
Priority (No,Kind,Date): US 467148 A 900118
Applic (No,Kind,Date): JP 90311677 A 901119
IPC: * H04N-001/40; G06J-003/00; H04M-011/00

Language of Document: Japanese

UNITED STATES OF AMERICA (US)

Patent (No,Kind,Date): US 5630153 A 970513

INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED
INTERNAL MEMORY (English)

Patent Assignee: NAT SEMICONDUCTOR CORP (US)

Author (Inventor): INTRATER AMOS (HK); DORON MOSHE (US); INTRATER
GIDEON (IL); EPSTEIN LEV (IL); VALENTATEN MAURICE (DE); GREISS
ISRAEL (IL)

Priority (No,Kind,Date): US 317783 A 941004; US 11102 B1 930129;
US 467148 B1 900118

Applic (No,Kind,Date): US 317783 A 941004

National Class: * 395800000; 395290000; 395306000; 364DIG001;
364DIG002

IPC: * G06F-009/26; G06F-009/40; G06F-009/44; G06F-013/36

Derwent WPI Acc No: * G 91-246848; G 98-263467

Language of Document: English

UNITED STATES OF AMERICA (US)

Legal Status (No,Type,Date,Code,Text):

US 5630153 P 900118 US AA PRIORITY
US 467148 B1 900118

US 5630153 P 930129 US AA PRIORITY
US 11102 B1 930129

US 5630153 P 941004 US AE APPLICATION DATA (PATENT)
(APPL. DATA (PATENT))
US 317783 A 941004

US 5630153 P 970513 US A PATENT

US 5630153 P 990323 US RF REISSUE APPLICATION FILED
(REISSUE APPL. FILED)
990120

LEVEL 1 - 1 OF 1 PATENT

5,630,153

<=2> GET 1st DRAWING SHEET OF 8

May 13, 1997

LEXIS-NEXIS

Library: PATENT

File: ALL

Integrated digital signal processor/general purpose CPU with
shared internal memory

REISSUE: Reissue Application filed Jan. 20, 1999 (O.G. Mar. 23, 1999) Ex. Gp.:
1641; Re. S.N. 09/234,427

CORE TERMS: general purpose, memory, digital, processor, array, bus, module,
operand, bit, data processing...

5,630,153 OR 5630153

LEXIS-NEXIS
Library: PATENT
File: CASES

Your search request has found no CASES.

To edit the above request, use the arrow keys. Be sure to move the cursor to the end of the request before you enter it.

To enter a new search request, type it and press the ENTER key.

What you enter will be Search Level 1.

For further explanation, press the H key (for HELP) and then the ENTER key.

5,630,153 OR 5630153

LEXIS-NEXIS
Library: PATENT
File: JNLS
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Your search request has found no ITEMS.

To edit the above request, use the arrow keys. Be sure to move the cursor to the end of the request before you enter it.

To enter a new search request, type it and press the ENTER key.

What you enter will be Search Level 1.

For further explanation, press the H key (for HELP) and then the ENTER key.

-1- (LEGSTAT)

PATENT NUMBER US 5630153 [US5630153]

DOCUMENT TYPE US-P

ACTION 94.10.04 US/AE-A

APPLICATION DATA (PATENT)

{US 317783/94 [94US-317783] 94.10.04}

ACTION 97.05.13 US/A

PATENT

ACTION 99.03.23 US/RF

REISSUE APPLICATION FILED

990120

UPDATE 9915